

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Atty Dkt. 829-618

C# M#

YONEMARU, M.

C/A.U.

2826

Serial No. 10/720,764

Examiner: Dickey, T.

Filed: November 25, 2003

Date: October 18, 2004

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND FABRICATION METHOD FOR
SAMECommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE/AMENDMENT/LETTER

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

☐ **Correspondence Address Indication Form Attached.****Fees are attached as calculated below:**

Total effective claims after amendment	23	minus highest number			
previously paid for	23	(at least 20) =	0	x	\$ 18.00
					\$ 0.00

Independent claims after amendment	2	minus highest number			
previously paid for	3	(at least 3) =	0	x	\$ 88.00
					\$ 0.00

If proper multiple dependent claims now added for first time, add \$300.00 (ignore improper)	\$ 0.00
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Petition is hereby made to extend the current due date so as to cover the filing date of this paper and attachment(s) (\$110.00/1 month; \$430.00/2 months; \$980.00/3 months)	\$ 0.00
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Terminal disclaimer enclosed, add \$ 110.00	\$ 0.00
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<input type="checkbox"/> First/second submission after Final Rejection pursuant to 37 CFR 1.129(a) (\$790.00)	\$ 0.00
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☐ Please enter the previously unentered , filed☐ Submission attached**Subtotal \$ 0.00**

If "small entity," then enter half (1/2) of subtotal and subtract	-\$ 0.00
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☐ Applicant claims "small entity" status. ☐ Statement filed herewith

Rule 56 Information Disclosure Statement Filing Fee (\$180.00)	\$ 0.00
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Assignment Recording Fee (\$40.00)	\$ 0.00
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Other:	0.00
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TOTAL FEE ENCLOSED \$ 0.00

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

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HWB:lsHNIXON & VANDERHYE P.C.
By Atty: H. Warren Burnam, Jr., Reg. No. 29,366Signature: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
YONEMARU, M.

Serial No. 10/720,764

Filed: November 25, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND
FABRICATION METHOD FOR SAME

* * * * *

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

ELECTION UNDER 35 USC §121

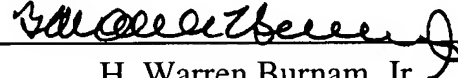
In response to the Office Action dated September 16, 2004 holding the subject matter of claims 21-23 to be non-obvious and patentably distinct from that of claims 1-20, Applicant(s) hereby elect the invention of Group II, (upon which claims 1-20 are readable) for further substantive examination.

This election is made without traverse. However, since a restriction requirement is never proper unless the restricted group of claims is patentably distinct (i.e., inter alia, non-obvious under 35 USC §103) from the elected group of claims, the Examiner is requested to insure that such patentable distinctness is present before proceeding to make the requirement final.

It is respectfully requested that the non-elected claims be retained for use with a possible divisional application.

Respectfully submitted,
NIXON & VANDERHYE P.C.

October 18, 2004

By: 
H. Warren Burnam, Jr.
Reg. No. 29,366

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